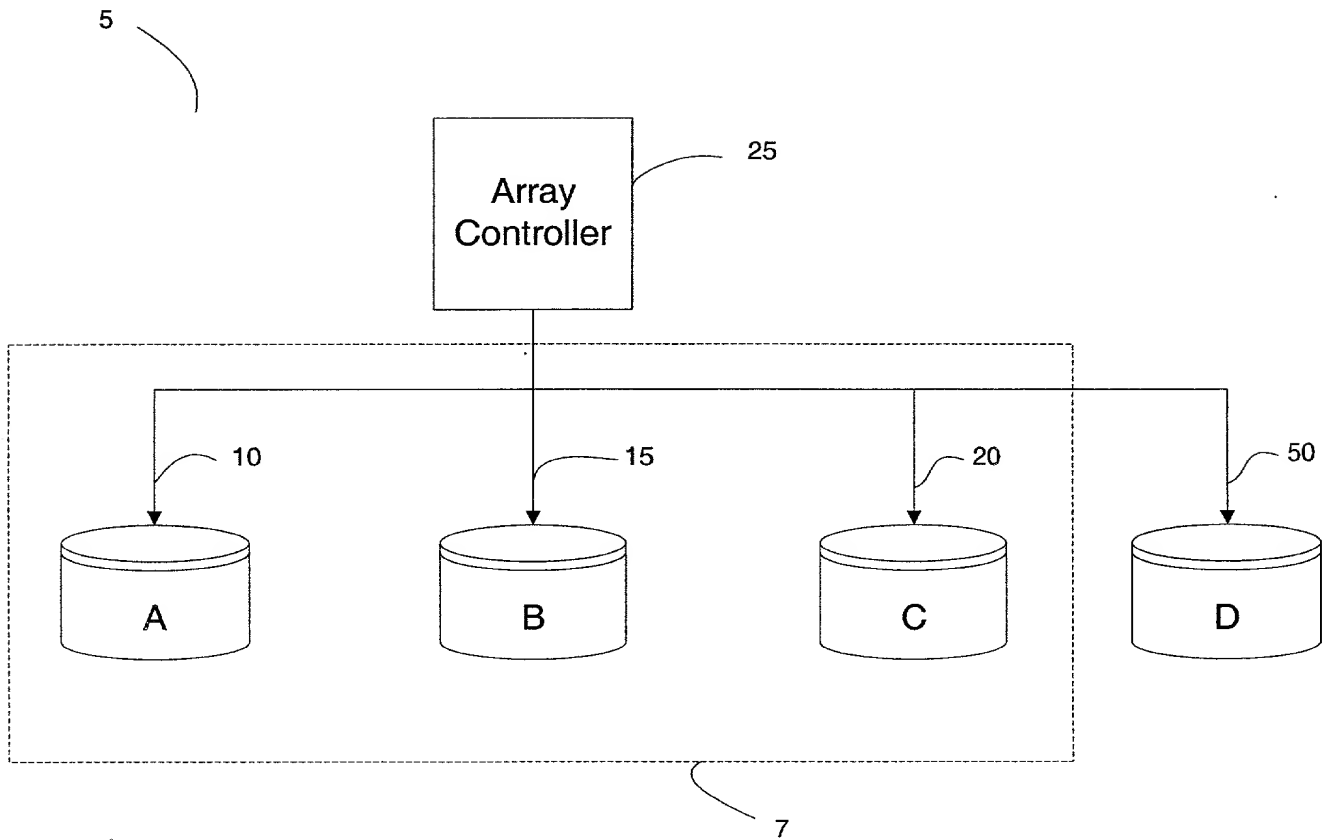


Fig. 1 is a block diagram of a prior art system 5. The system 5 includes an array controller 25 connected to a storage array 7. The storage array 7 includes four storage devices 10, 15, 20, and 50, labeled A, B, C, and D respectively. The array controller 25 is connected to each of the storage devices 10, 15, 20, and 50 via a common bus.



Prior Art

Fig. 1

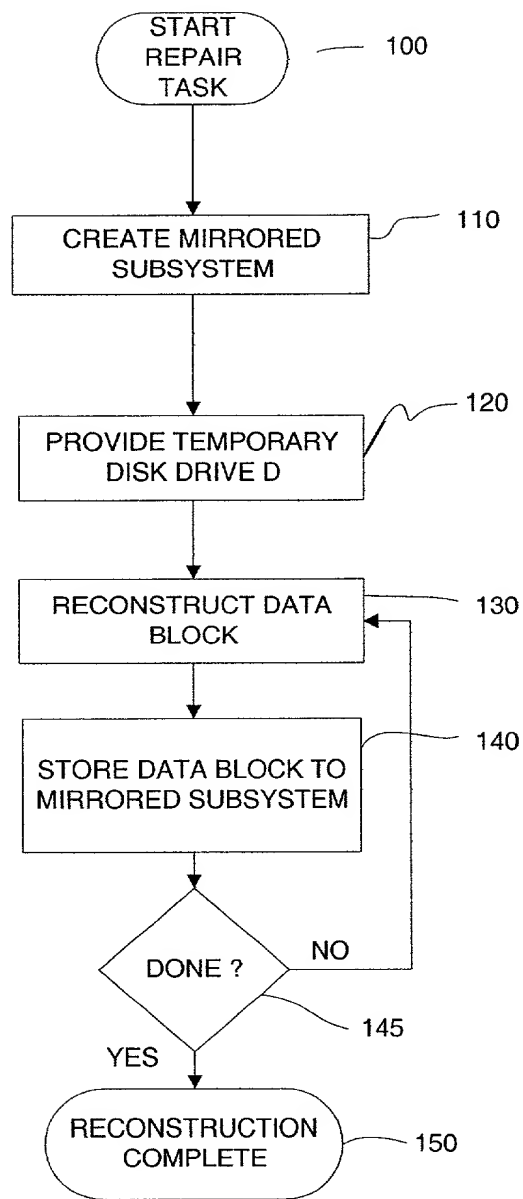


Fig. 2

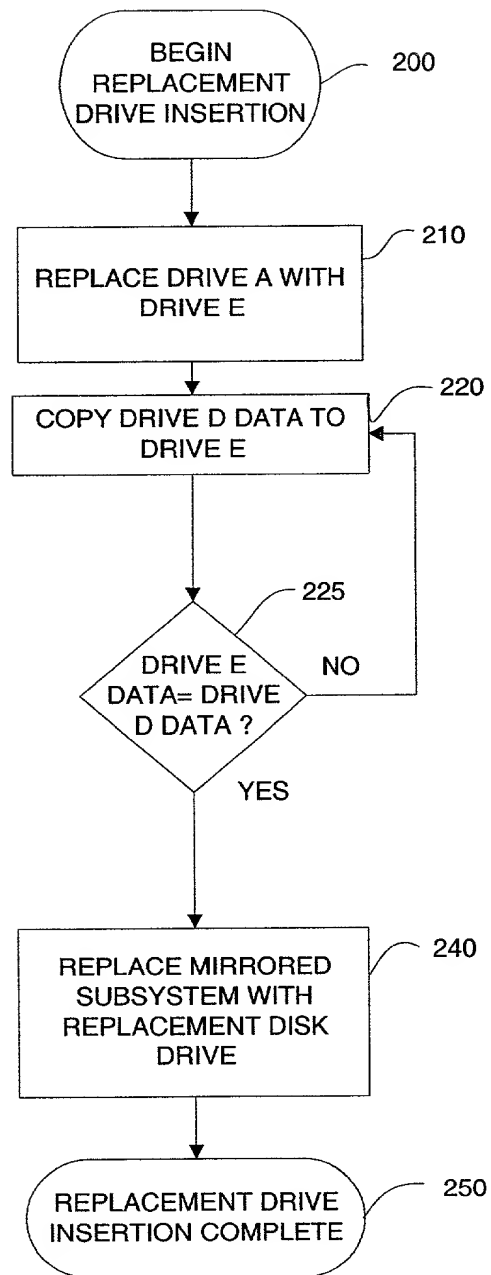


Fig. 3

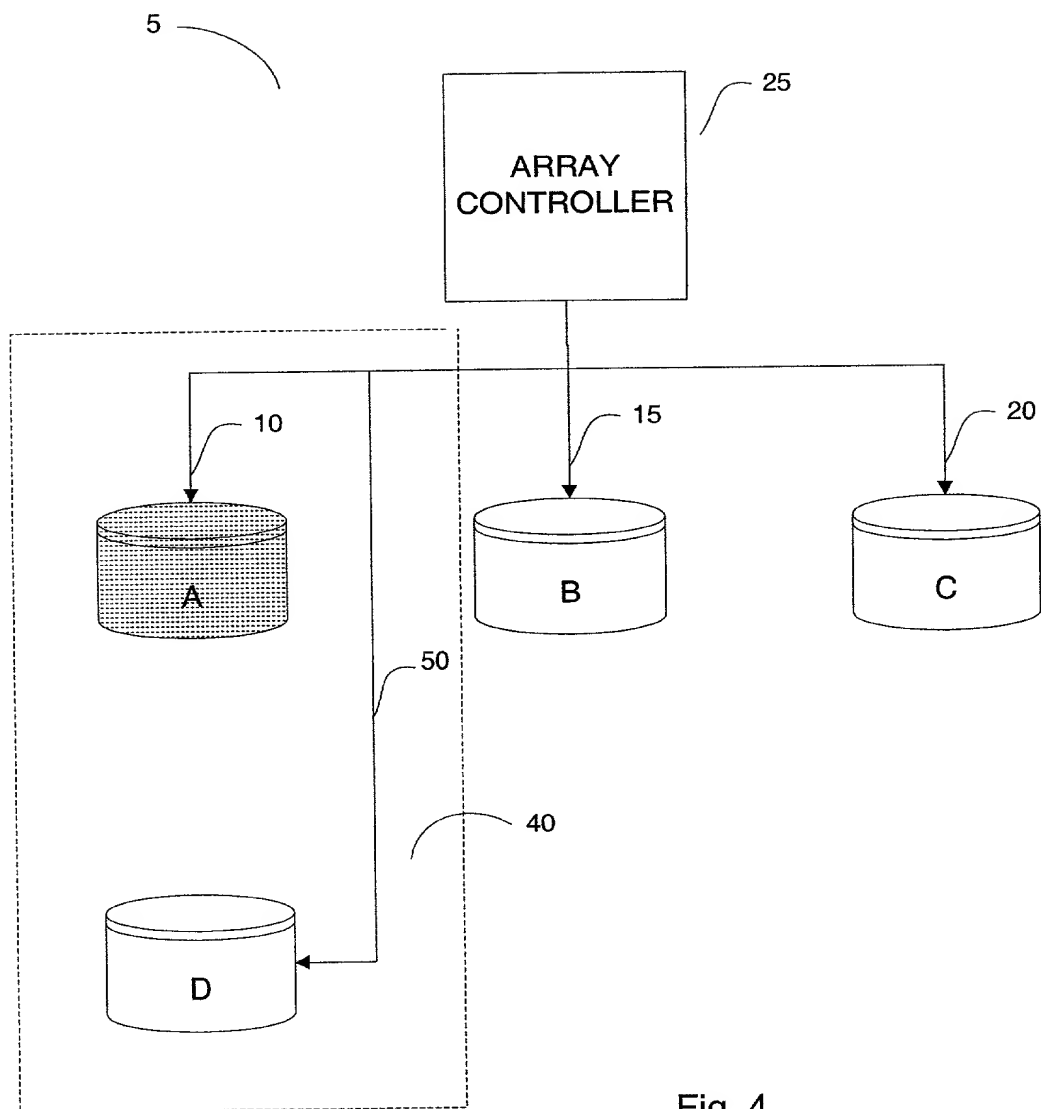


Fig. 4

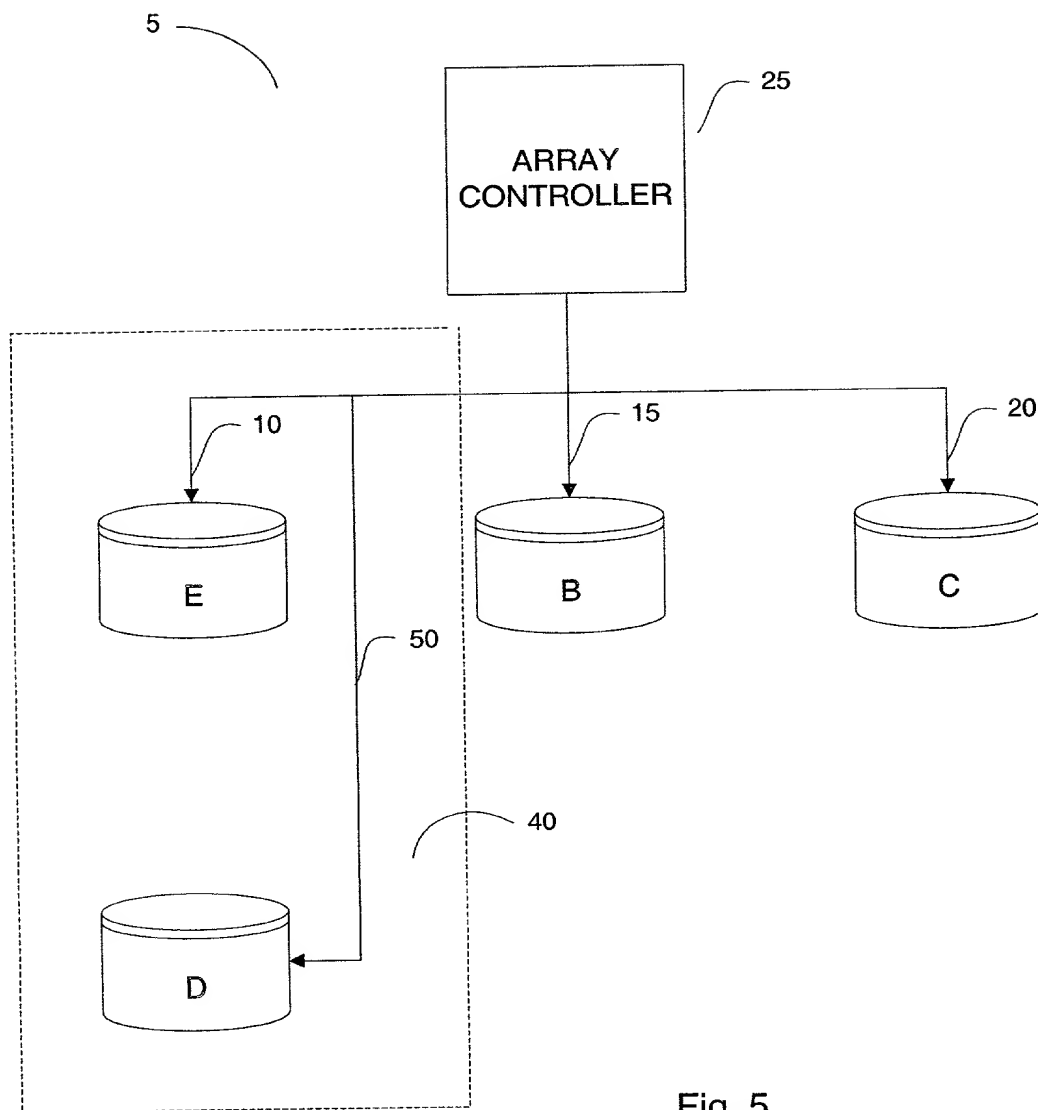


Fig. 5

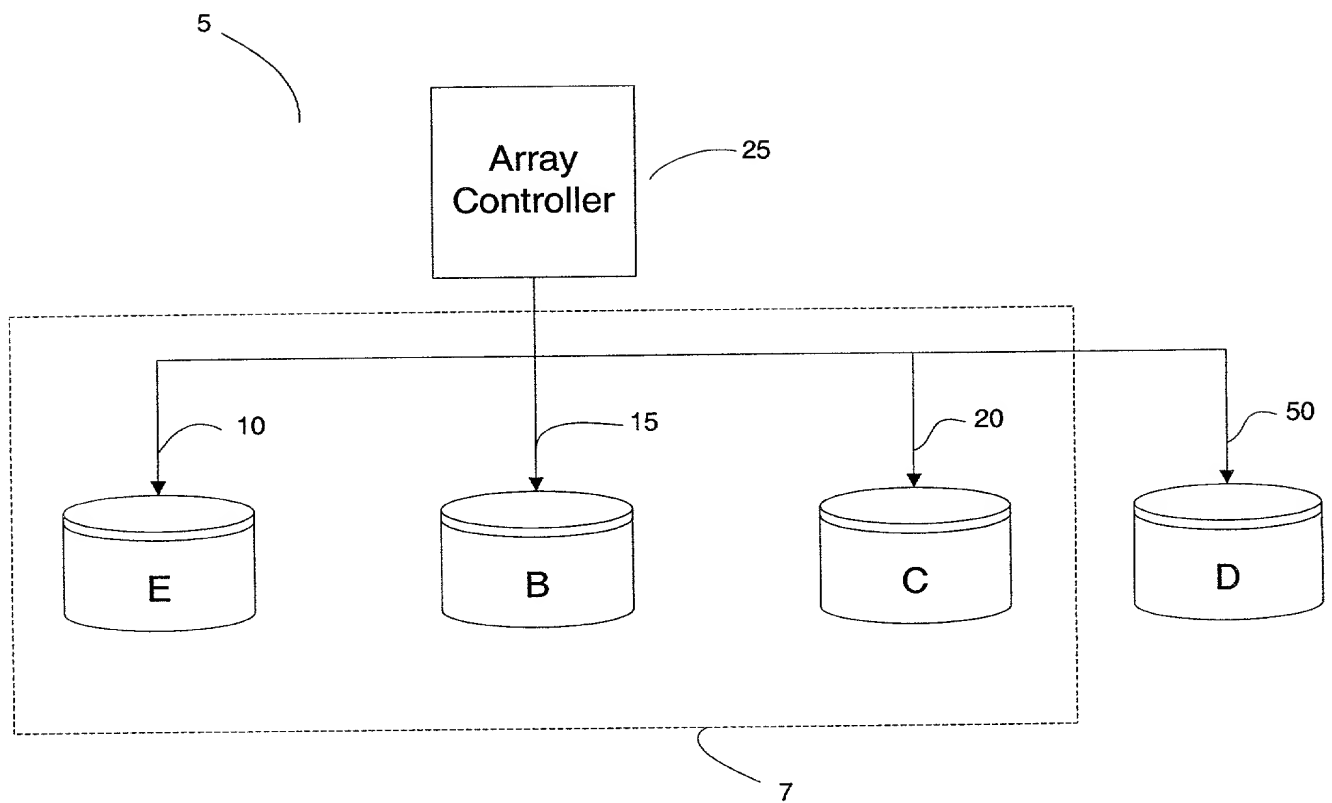
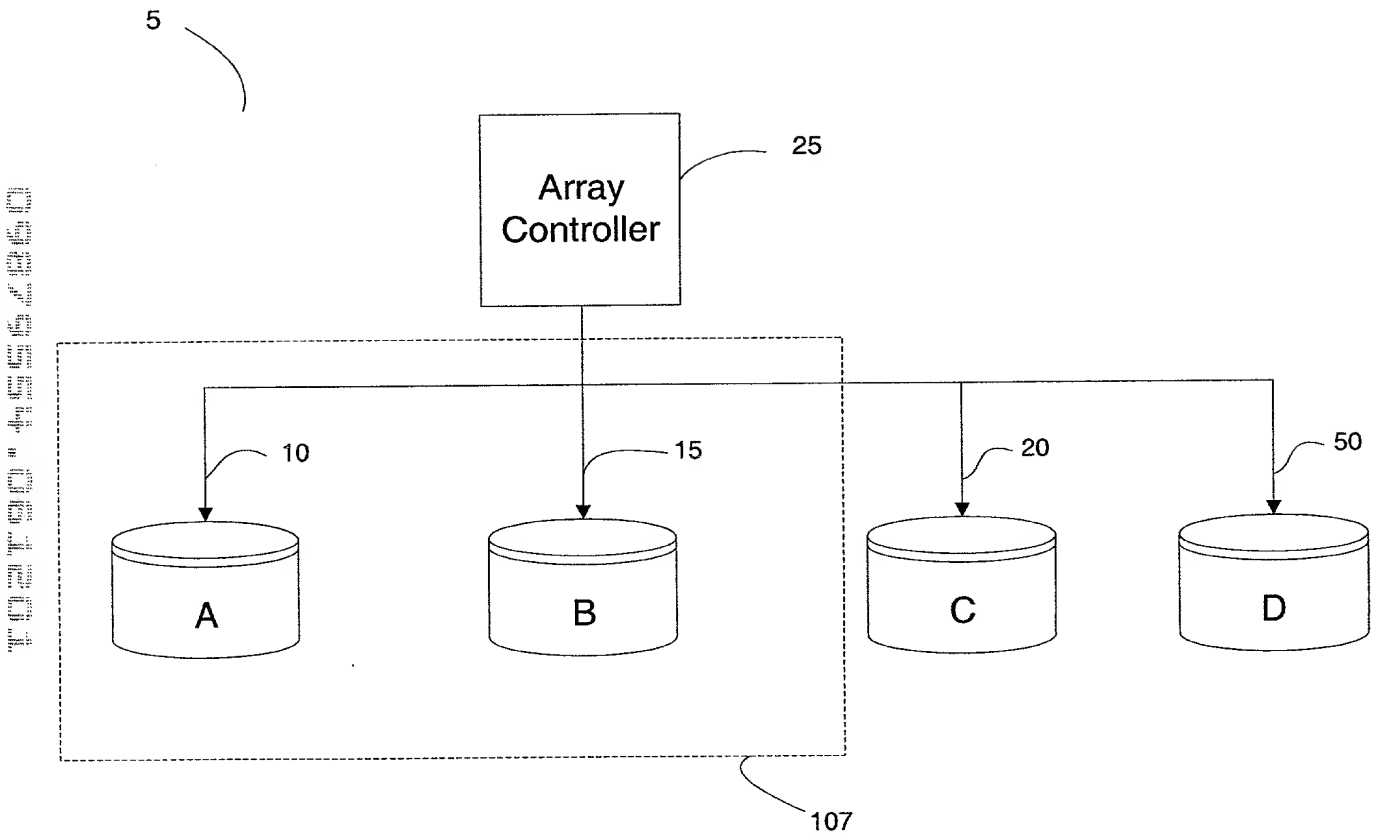


Fig. 6



Prior Art

Fig. 7